



Design and Development of a Digital Controlled Dielectric Barrier Discharge (DBD) AC Power Supply for Ozone Generation

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A digital controlled dielectric barrier discharge (DBD) AC (Alternative current) power supply is designed and investigated. The power source design with a diode bridge rectifier and PWM (Pulse Width Modulation) inverter along with driver circuits are presented. A step-up transformer is designed to carry 4.6 kW and 10 kVp-p for a dielectric barrier discharge (DBD) AC power supply and ozone generation. An STM (STMicroelectronics) microcontroller is employed to control the phase shift angle of the PWM inverter. The operating frequency of the PWM inverter is 25 kHz. Zero voltage detection can be reached and achieves maximum efficiency. Also, a high voltage transformer is included. The practical results shown that the DBD power supply can be controlled at the chosen value and extreme efficiency can be 87.45 % at 4.6 kW/10 kVp-p.

Keywords: Control circuit, Diode bridge rectifier, High voltage transformer, PWM inverter, STM

Introduction

Dielectric barrier discharge (DBD) expertise schemes often require high voltage, AC (Alternative current) power systems.¹⁻³ Many of all in DBD plasma developments for environmental safety applications, like air, water, or exhaust treatment, so named DBD reactor topologies are used.^{4,5} DBD plasma schemes as loads are very application detailed, however, they can be categorized by nonlinear voltage-power features, occasionally, for assumed working conditions and aimed chemical reactions, an optimum of supply limitations (voltage, power) can be recognized.⁶ Modern developments contain power amplifiers (PA) with high-voltage transformers⁷ or divergent solid-state switch based converter topologies, generally resonant ones.⁸ Due to the resonant operation method, it is difficult to control the output power fluently; a time-averaged burst method is often used.⁹

A digital control power supply^{10,11} has attracted recent attention because of the lower price, good operation with improved and assimilated power electronic peripherals, such as ADC (Analog to Digital Converters) converters¹² and PWM (Pulse

Width Modulation). STM (STMicroelectronics)-based microcontroller allows the execution of extra efficient control outlines, standard control hardware schemes for several stages, and flexibility. Digital controllers are less prone to aging, are eco-friendly, and have good noise reduction. The 32-bit RISC core operates at a 48 MHz frequency. Also, it has a rapid embedded memory and a wide range of improved peripherals and I/O's. The whole devices proposal typical communication interfaces one 12-bit ADC, 7 general-purpose 16-bit timers as well as a progressive control PWM timer.

Ozone (O₃) is measured as an outstanding dominant oxidizer and sterilizer. Its decontamination potential is expressively greater than chlorine and other decontaminators. Currently, ozone is generally used for decontaminating and dissolving in exchange for chlorine, due to the latter by goods such as smell, bad taste, and carcinogenic causes resulting from it. Certainly, ozone creates much fewer by-products and ozone itself is converted into oxygen in limited hours. Applications of ozone expertise are many and could be established in decontamination, water, and air sanitization, drug, and so on.

Sugita¹³ achieved complete sterilization within 60–120 sec, highlighting the value of plasma as a

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more efficient instrument than autoclaving, which takes approximately one hour. 14. Summerfelt & Hochheimer¹⁴ proposed a design that can produce a high voltage yield. Moreover, the efficiency of PA (Power amplifier) is 50%. The overall power given to the load is only 1 W; hence, the potential harm of this power is insignificant. On the other hand, one problem is the current spike produced when the transistor switches OFF, which causes losses and places accumulation pressure on the transistor. Placing an extra capacitor in shunt¹⁵ with the load increased the total load capacitance, thereby reducing the resonating inductor condition. This technique is not necessary because it requires two supplementary mechanisms and incurs increased power loss. Another study¹⁶ did not use a resonating inductor. As an alternative, the system worked at a frequency near the resonance of the transformer and the capacitive load. Mastanaiah¹⁷ proposed a plasma torch initiator using an E power amplifier. The frequency of the procedure was 13.56 MHz and the output was up to 2 kV. On the other hand, this structure is large due to the current condition of the application. In the present article, we are using a digital control system for reducing the system cost, series resonant allowing the soft switching, reduction of harmonic distortion on the terminals of DBD devices, current & voltage protection circuits are designed for system protection and the system efficiency was 87.45%. These addresses have not been highlighted in the literature.

Comparison with Other Control Methods

Yang & Zhijian Dan¹⁸ authors designed high accuracy DC regulated power supply system based on a power amplifier and closed-loop feedback controller, and a fuzzy adaptive PID control algorithm has been used to adjust the output voltage/current. On the other hand Li *et al.*¹⁹ designed a digital high-voltage power supply system based on DSP control, simulated, and tested based on Matlab/Simulink/Simpower system platform. Rao *et al.*²⁰ recorded several approaches of digital control in practical implementation, such as PID controller, Type III compensator, and sliding mode controller that are designed to promote the dynamic performance of the power supply. Pu *et al.*²¹ gave a PID model and estimated by using MATLAB to investigate the performance of the system. The mathematical model is used to describe the control process of the beam voltage and current.

Recent technologies

In recent technologies, a new type of all-solid-state bipolar high-level multilevel generator is proposed²² and the main energy storage capacitor of this structure is charged by a high-frequency resonant power supply after uncontrolled rectification, and the bipolar pulse output can be exploited by using each submodule. A high voltage DBD plasma reactor has been developed²³ using an iron plane high voltage electrode, a stainless steel ground electrode, and a glass dielectric, controlled by high voltage power supply of up to 15 kV using a high voltage step-up transformer. The study of the square current source, considering the effects of the parasitic capacitance and magnetizing inductance is reported by Rueda *et al.*²⁴, and a numerical technique is proposed and validated for finding the working ratings and waveforms of the converter. A low-budget pulsed high voltage solid-state chopper has been developed with the modular and scaled design²⁵, and the high voltage isolation is attained by means of the magnetic decoupling. To address the issue, a wide range frequency model of OGs is presented by Tang *et al.*²⁶, because the current through OGs is substituted by a sinusoidal current source with variable amplitude and frequency.

Compared with additional control techniques and recent scientific trends, this system is designed with a low cost of STM microcontroller. The efficiency of the controlled system is quite high. We address the driver circuits and control circuits in this article and in the driver circuit we have used zero voltage detectors and line regulation circuits. Both circuits are used to synchronize the microcontroller with the AC input signals and the ability of the power supply to preserve its listed output voltage over the deviations in the input line voltage. This is stated as the percentage variation in the output voltage compared to the variation in the input line voltage.

The goal of this research article is the development of a digital controlled DBD AC power supply for ozone generation, consisting in a power stage, a diode bridge rectifier, inverter circuit, and control design, where the output frequency could be simply controlled by STM microcontroller.^{27,28} A laboratory experiment was carried out to study the decontamination effectiveness of an ozone generator provided by this DBD design.

In this study, the development of a digital controlled DBD AC power source for ozone

generation is shown. An AC power supply system is designed with digital control. STM microcontroller is used in this control system. The alternative high voltage output is connected with the DBD load. This power supply system using for ozone generation applications. The system achieves 87.45 % efficiency.

Digitally Controlled AC Power Supply

A variable AC voltage is employed as the input voltage to the system. A diode bridge rectifier circuit was developed as the early stage of the AC power supply; it converts 220V AC to a DC (Direct Current) voltage²⁹⁻³⁶ which can be varied from 0 V to 300 V by a user. An inverter topology was developed and placed, which takes the DC voltage from rectifier output and varies it to a high- frequency AC output voltage. A step-up transformer³⁷⁻⁴¹ was developed to achieve high voltage at the output side and the 10 kV output side of the transformer was associated with the DBD load. A zero voltage detector circuit was used to synchronize the microcontroller with the AC input signals. The STM microcontroller was used for programming the online regulation, over-voltage protection, over current protection, soft-start, and PWM.

AC Power Source Circuit

The simplified diagram for digital control DBD AC power supply is briefly explained below in different sub-parts.

Diode bridge rectifier

A high voltage single-phase diode bridge rectifier was designed with four high voltage diodes, as shown in Fig. 1. The bridge rectifier converts AC power into DC power. In this design, the DC link capacitors placed following by the bridge rectifier.

Design of PWM inverter

A PWM inverter is developed and placed by the after DC link capacitors as presented in Fig. 2 (a). The IXFK64N50P MOSFET was used for substituting the inverter circuit. This MOSFET was used as a switching device in the PWM full-bridge inverter.⁴²⁻⁴⁴ The chosen MOSFET allowed a 500 V and 64 A maximum. This has a lower drain-source resistance (85 m ohms) and a fast intrinsic diode. The MOSFET switches are switched alternately with a controlled duty ratio to convert input DC voltage into high-frequency AC voltage suitable for exciting the DBD load.

The developed inverter driver circuit produces two opposite PWM signs working at 25 kHz with a 0.5 μ s dead band. The driver IC (Integrated Chip) “IXDN604SIA” is a dual high rapidity gate driver that is particularly well suitable for driving MOSFETs. The input of each driver is protected effectively from latch-up, and the named circuitry removes the annoying conduction and current shoot from side to side. When the Q3 and Q6 MOSFET switches were turned ON and the Q4 and Q5 switches were turned off, a positive DC voltage was gained at the output. A negative DC voltage was gained at the output when the Q4 and Q5 switches were turned ON and the Q3 and Q6 switches were turned off. The MOSFET switches were switched consecutively with a well-ordered duty ratio to alter the input DC voltage to a high-frequency AC voltage⁴⁵⁻⁵⁰ appropriate for stirring the DBD load.

The inverter is measured with phase shift signals. To function the phase-shifted PWM inverter under a zero voltage detecting circuit, the working frequency

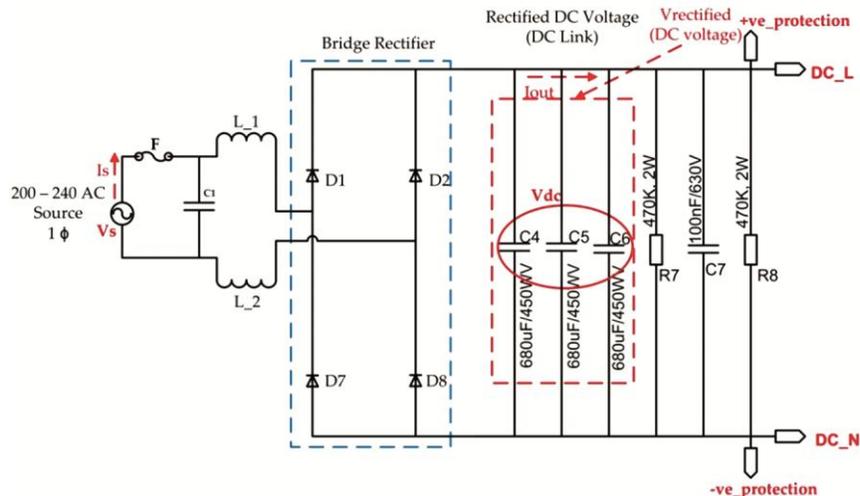


Fig. 1 — Schematic diagram of diode bridge rectifier for digital control plasma switching power supply

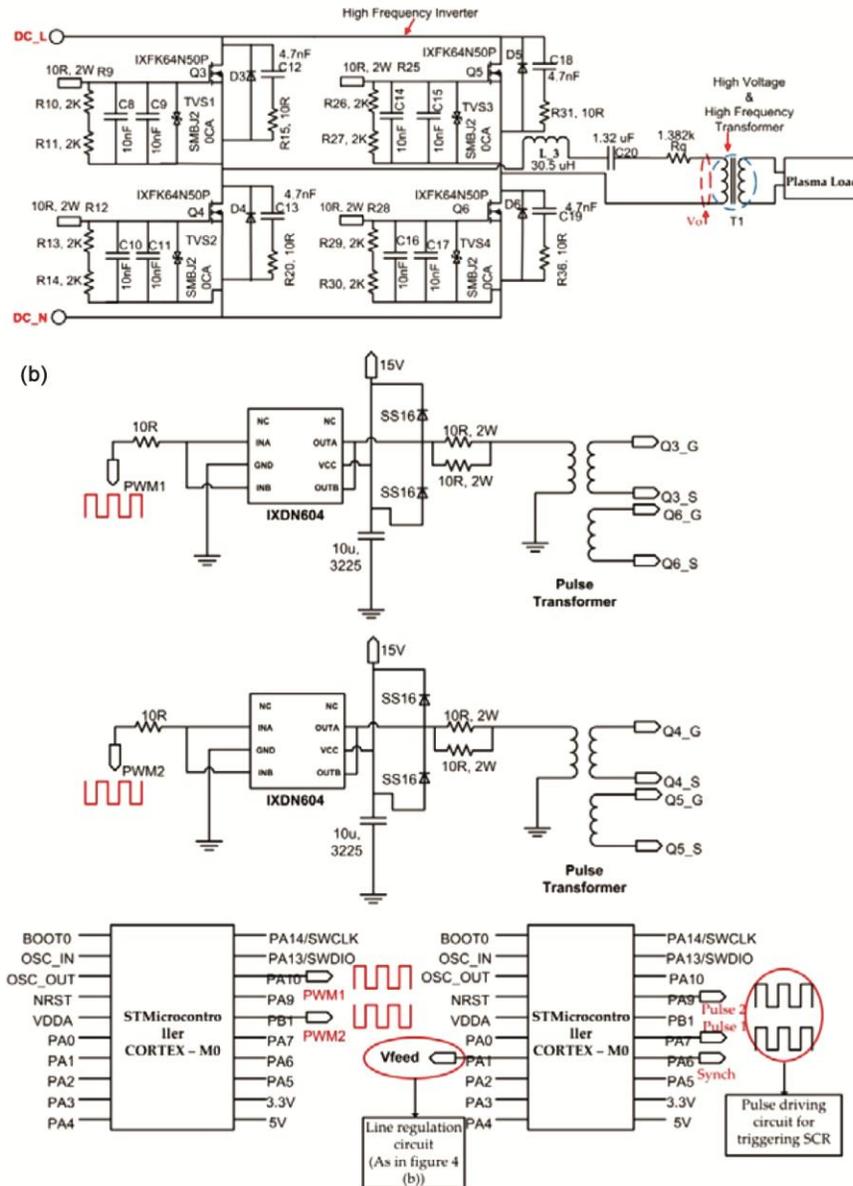


Fig. 2 — Schematic diagram of PWM inverter for digital control DBD AC power supply (a) power source; (b) Driver circuit with STM microcontroller

must be greater than the resonant tank. The resonant tank is associated with the main side of the transformer. There were four major operation modes. The power transferred to the DBD could be resolute over varying phase angles, Φ_i , between VQ6 and VQ3, where $\Phi_i = VQ6 < -VQ3 = VQ5 < -VQ4 <$. The higher the phase shift angle Φ_i is, the lesser the amplitude of the resonant voltage and output power turn out to be; and the lesser the phase shift angle Φ_i is, the greater the amplitude of the resonant sinusoidal voltage and output power carried to the DBD turn out to be. The time break, T is from T_0 to T_{iv} & is distinct

as a single period. The angle, α_i is the phase angle, of which the current signal, i_o is lagging to the voltage signal, V_o . The circuit procedure equivalent to the switching designs can be investigated in four operation methods.

- Operation method I - ($T_0 - T_i$): VQ3 and VQ5 are high and $i_o < 0$ in this method. D3& Q5 are turned on. The voltage on Q3 is “0”, the value of i_o is reduced. At $t=T_i$, i_o matches “0” and this method ends.
- Operation method II - ($T_i - T_{ii}$): VQ3 and VQ6 are high and $i_o > 0$ in this method. Q3 & Q6 are turned

on. The voltage on Q3 is “0” and Q3 attains a ZVC at T_i . At $t=T_{ii}$, i_o extends the determined value, and this method ends.

- Operation method III - (T_{ii} - T_{iii}): VQ4 and VQ6 are high and $i_o > 0$ in this method. D4 & Q6 are turned on. The voltage on Q4 is “0” and the value of i_o is reduced. At $t=T_{iii}$, i_o matches “0” and this method ends.
- Operation method IV - (T_{iii} - T_{iv}): VQ4 and VQ5 are high $i_o < 0$ in this method. Q4 & Q5 are turned on. The voltage on Q4 is “0” and Q4 attains a ZVC at T_{iii} . At $t=T_{iv}$, i_o extends the determined value, and this method ends.

High voltage transformer design

The ferrite core transformer is designed with a C-shaped transformer core. Power ferrites are engaged as the magnetic substantial due to the more resistivity and small eddy current damages under high frequency. PL-13, U is chosen to provide the large winding turns and to achieve the insulation condition. The transformer turns are N_p & N_s , which yields $N_p=23$, $N_s=770$.

Core cross sectional area, $A_e = A * B = 30.3 * 18 = 545.4 \dots (1)$

Eq. (1) substitute in Eq (2) formula to determine the number of turns per volt, T_e is:

$$\frac{42}{\text{Core Area}} = \text{number of turns per volt, } T_e \dots (2)$$

Number of turns per volt, $T_e = 0.077 \dots (3)$

Input required voltage *
number of turns per volt = Primary turns, $N_p \dots (4)$

From Eq (3) and (4) substitute in below formula
Primary turns, $N_p = 23 \dots (5)$

Based on faradays law,

$$V = L \frac{di}{dt} = N \frac{d\phi}{dt} \dots (6)$$

$$V = N * A_e \frac{dB}{dt} \dots (7)$$

Maximum flux density, $B_{max} = \frac{\phi}{A_e} \dots (8)$

Switching frequency is $F_s = 25\text{kHz} \dots (9)$

From Eq (1), (8) and (9),
 $N_p = \frac{V_s * 10^4}{4 * B_{max} * A_e * F_s} \dots (10)$

$$N_s = 33.33 * N_p \dots (11)$$

Litz wire used at the primary side of the transformer. A copper wire is used at the secondary side of the transformer and results in a minor leakage inductance. Primary winding — 0.2 mm Litz wire, 3 A/mm²; Secondary winding — AWG16 high voltage cable copper wire, 1 A/mm².

The developed resonant frequency is around 25 kHz, and the inductor is 30.5 uH. Toprovide a high voltage to the DBD, the transformer of the turn’s ratio is 23 turns and 770 turns.

Control Circuits for Digital Plasma

The STM32F030F4P6 microcontroller IC was manufactured by STMicroelectronics. STM32F030F4P6 is a minimum system board (cortex-M0). The microcontroller frequency up to 48 MHz, high-speed embedded memories, anda wide range of enhanced peripherals and I/Os. All devices offer standard communication interfaces, one 12-bit ADC, seven general-purpose 16-bit timers, and an advanced-control PWM timer. The PLL is not enabled, thus the external clock drives the Cortex-M0 CPU directly.

The controller was realized with a low-cost single-chip 32-bit MCU(microcontroller unit) controller. This MCU mainly designed for cost-effective applications. STM32F030F4P6 MCUs association real-time performance and advanced architecture. As a minimal ready-to-run system, this board integrates micro USB(Universal Serial Bus) power supply interface, ISP/SWD(In-System Programming/Serial Wire Debug) programming/debugging interface, and boot mode selection and so on to take you into the ARM (Advanced RISC Machine) Cortex world easily. This IC allows synchronization of the AC input signals, regulation of the designed system, and varying the phase position of the PWM waveforms produced by the STM microcontroller.

The control circuits for the digital plasma power supply are represented in Fig. 3. The zero voltage detection circuit is shown in Fig. 3(a). Zero voltage detectors are used to synchronize the microcontroller with the AC input signals.

The line regulation circuit is presented in Fig. 3(b). The line regulation is the ability of the power supply to preserve its listed output voltage over the deviations in the input line voltage. This is stated as the percentage variation in the output voltage compared to the variation in the input line voltage.

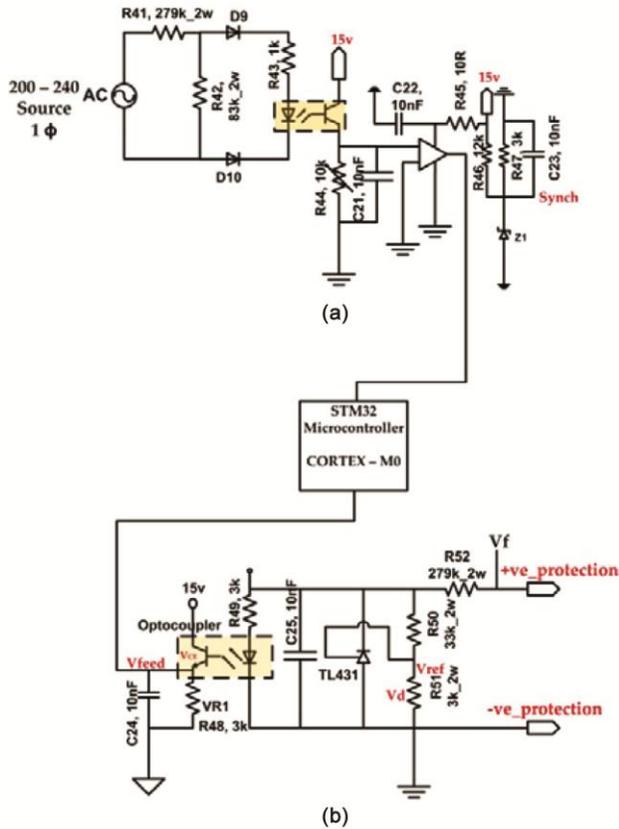


Fig. 3 — (a) Zero voltage detector circuit; (b) Line regulation circuit)

Setup Execution and Parameters

The proposed digital controlled DBD AC power supply was verified experimentally. The single-phase 220VAC, 60Hz power was used as an input to the circuit. The designed system is made with an inverter that is designed with “IXFK64N50P” MOSFET switches and the driver circuit is designed with the “IXDN604SIA” gate drive ICs. This inverter is connected with a transformer to increase the boosting up the output voltage.

Full-bridge phase-shifted inverter model was designed and verified with a rated power of 5000 W. The main conditions and the range of components are listed in Table 1. The rectified DC voltage was 300 V, and a MOSFET with 500 V and 64 A was selected. The considered resonant frequency was 25 kHz. To supply a high voltage to the DBD, a transformer with a high step-up ratio was used.

Simulation Results

The simulation results of the digital controlled DBD AC power supply by using Psim (Power sim) software are presented in Fig. 4. The single-phase

Table 1 — Specifications of digital control AC power supply

Parameter	Ratings
Input voltage	200 V AC-240 V AC
Output power	4600 W
Output voltage	10 kV _{p-p}
Switching frequency	25 kHz
DC link voltage	300 V
MOSFET switches	IXFK64N50P
Series resonant inductor L ₃	30.5 uH
DC link capacitor	2040 uF
Transformer turns ratio	23:770
Transformer leakage inductance	33.33 uH

200–240 V AC is applied as an input to the diode rectifier and followed by the DC link. The DC power is applied as an input to the IGBT inverter and converts DC into AC power. The IGBT inverter provides the alternating voltage and current to the transformer. The high voltage transformer and rectifier have the task of generating voltage up to 10 kV_{p-p}. The inverter operating frequency is 25 kHz. The input voltage (V_{ac}), DC voltage (V_{dc}), rectifier voltage (V_{rec}), and waveforms of gate signals (V_{q3q6} , V_{q4q5}) are shown in Fig. 4(a). The primary voltage of the transformer (V_{prim}) and the output voltage of power supply (V_{out}) are shown in Fig. 4(b).

In this design, the simulation is carried out to prove the working operation. ZCS turn on & ZVS turn off of the IGBT’s can be reached by using the resonant circuit. The starting current of resonant topology can be partial.

Experimental Setup and Real-time Results

The experimental setup of the power supply for DBD is presented in Fig. 5(a). The power supply working is discussed below based on real-time results. To verify the circuits, the results are carried out with a variable AC voltage and different input power.

The output voltage (V_o) waveform of the transformer secondary side is employed to the DBD load, as shown in Fig. 5(b), while the output current signal (i_o) waveform of the transformer secondary side flows through the DBD load as shown in Fig. 5(c). The high-frequency PWM inverter output waveforms and gate signals are shown in Fig. 5(d). The inverter output and high-frequency square wave voltages were obtained from the DC input voltage from the bridge rectifier circuit as shown in Fig. 5(e) and it shows the two complementary 50% duty cycle PWM signals produced by the digital PWM controller. The two complementary PWM control

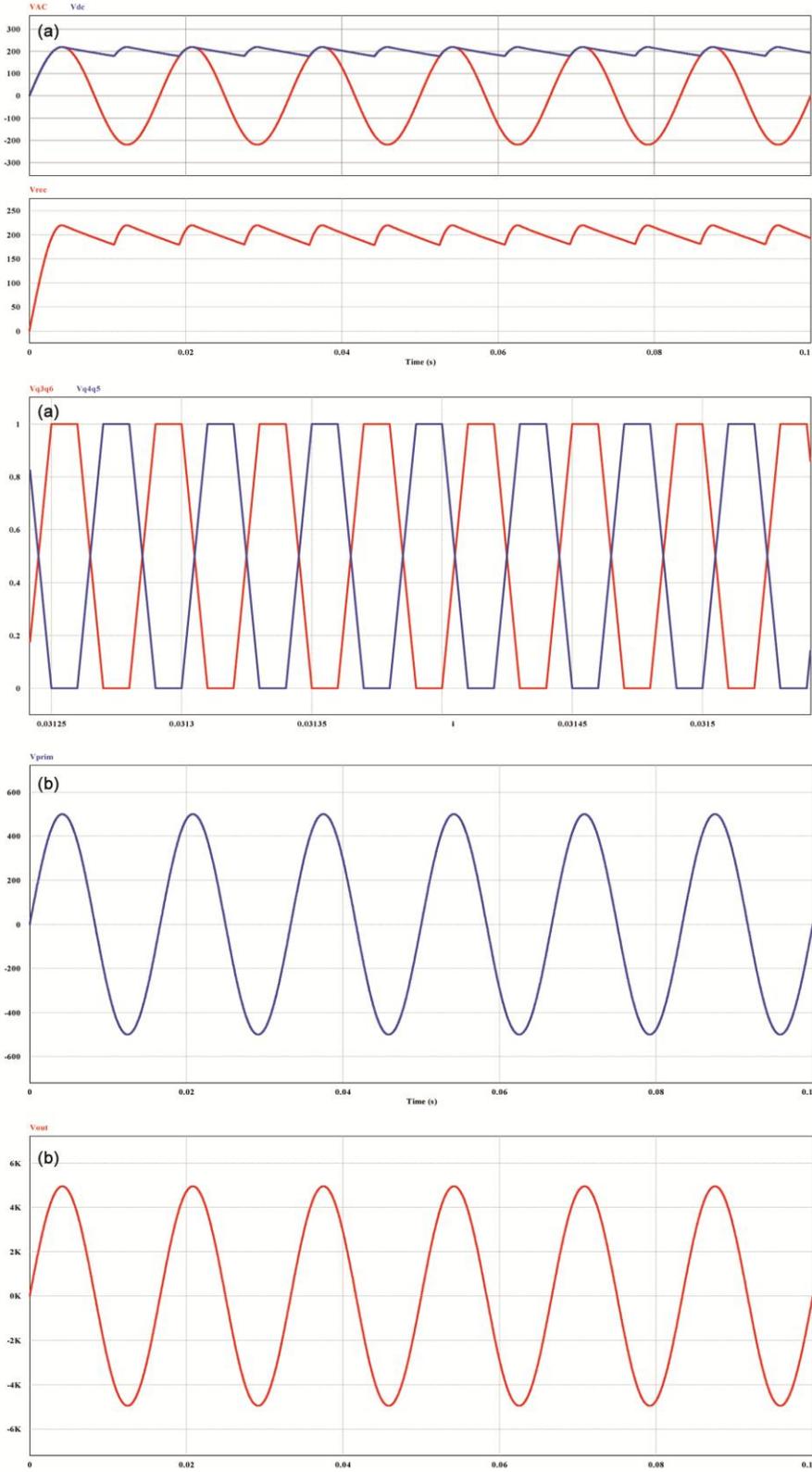


Fig. 4 — Simulation results of power supply circuit: (a) input power supply (V_{AC}), DC voltage (V_{DC}) and rectifier voltage (V_{rec}); (b) Transformer primary voltage (V_{prim}), and output voltage (V_{out})

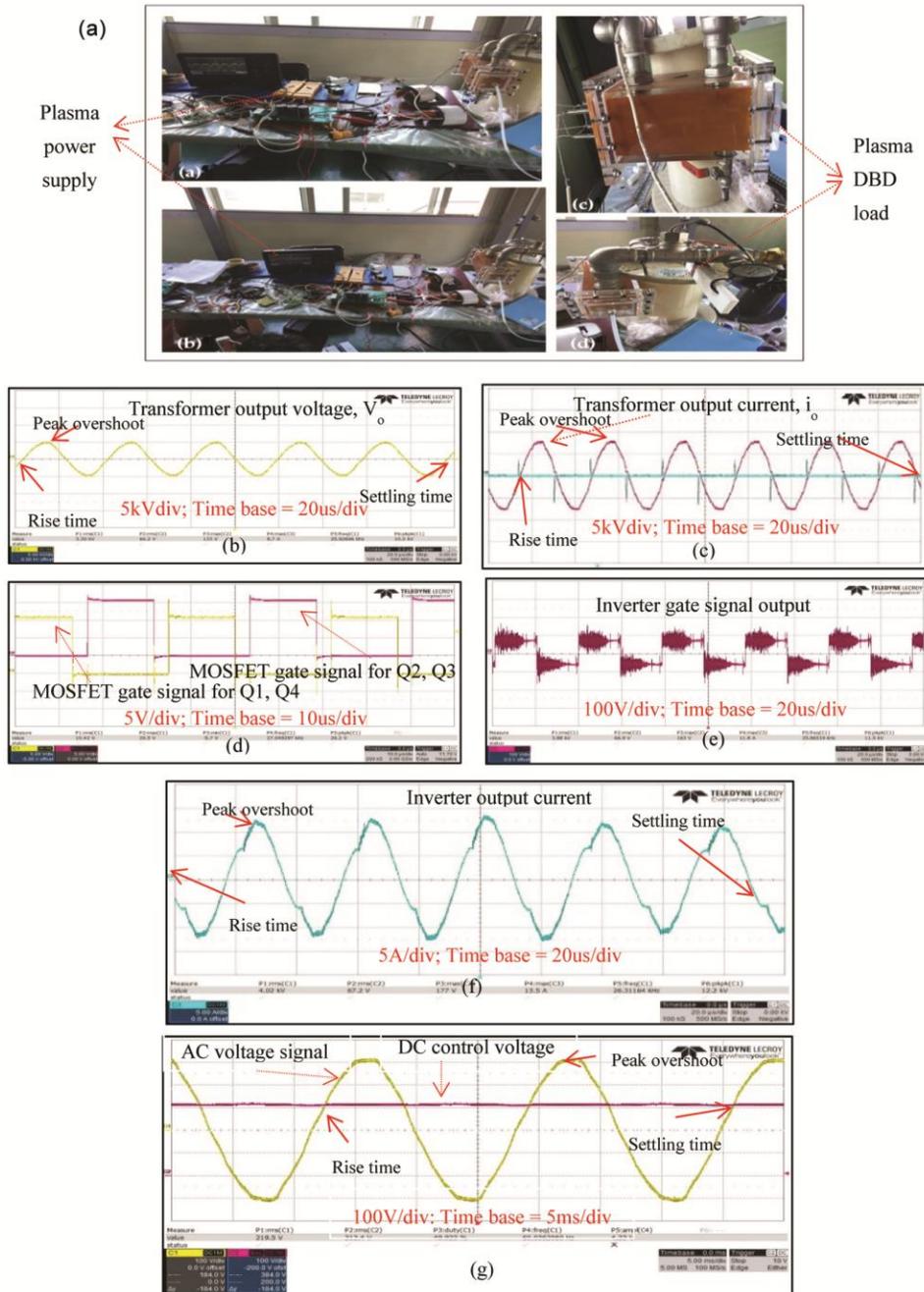


Fig. 5 — Real time results of plasma DBD power supply: (a) Experimental setup; (b) transformer output voltage, V_o (power supply output); (c) transformer output current, i_o ; (d) inverter gate signals; (e) inverter gate signal output; (f) inverter output current; (g) DC voltage from the single phase AC sinewave with different input power

signals are synchronized with each other and consist of a dead band, which avoids short circuits in full bridge. The controlled PWM signals were applied to the gate terminal of the MOSFET switches to switch the MOSFET on and off. The output current waveform of inverter (5 A/div) is shown in Fig. 5(f). The output current of the inverter side was changed to

a sinewave because of the series-connected filter. The DC voltage is converted from the single-phase AC sine wave with different input power as shown in Fig. 5(g).

Experimental Results

Below are the calculations for output power carried to the load and efficiency of the system.

The voltage applied to the resonant tank V_o is

$$V_o = V_{mi} \left[\cos \left(\omega_{sw}t - \frac{\Phi}{2} \right) + \cos \left(\omega_{sw}t + \frac{\Phi}{2} \right) \right] = 2V_{mi} \cos \left(\omega_{sw}t \right) \cos \left(\frac{\Phi}{2} \right) \frac{4}{\pi} V_{dc} * \cos \left(\omega_{sw}t \right) \cos \left(\frac{\Phi}{2} \right) \dots (12)$$

The voltage RMS value is

$$V_{o,rms} = \frac{\frac{4}{\pi} V_{dc} * \cos \left(\omega_{sw}t \right) \cos \left(\frac{\Phi}{2} \right)}{\sqrt{2}} = \left(\frac{2\sqrt{2}}{\pi} \right) V_{dc} \cos \left(\frac{\Phi}{2} \right) \dots (13)$$

The standardized switching angular frequency is defined as $\omega_{Nr} = \omega_{sw} / \omega_{ra}$, where ω_{ra} is the series resonant angular frequency. The switching angular frequency as $\omega_{sw} = 2\pi * f_{sw}$, where f_{sw} is the switching frequency.

The RMS value of the current “ i_o ” employed to the resonant tank is

$$i_{o,rms} = \frac{V_{(o,rms)}}{|Z_e|} = \frac{\left(\frac{2\sqrt{2}V_{dc} \cos \left(\frac{\Phi}{2} \right) \cdot \cos \alpha_i}{\pi} \right)}{2\sqrt{2}V_{dc} \cos \left(\frac{\Phi}{2} \right)} \dots (14)$$

$$(\pi) \cdot \sqrt{\left(1 + P^2 \right) \cdot \left(\omega_{Nr} - \left(\frac{1}{\omega_{Nr}} \right) \right)^2}$$

The load resistance R_q is,

$$R_q = \frac{V_{o,rms}}{i_{o,rms}}$$

The load resistance is, $P_{out} = V_{o,rms} * i_{o,rms} * \cos \alpha_i = i_{o,rms}^2 * R_q$

$$= \frac{\left(\frac{2\sqrt{2} \cdot V_{dc} \cdot \cos \alpha_i \cdot \cos \left(\frac{\Phi}{2} \right)}{\pi} \right)^2 \cdot R_q}{2\sqrt{2} \cdot V_{dc}^2 \cdot \cos^2 \left(\frac{\Phi}{2} \right) \cdot R_q} \dots (15)$$

$$(\pi^2) \cdot \left(1 + P^2 \right) \cdot \left(\omega_{Nr} - \left(\frac{1}{\omega_{Nr}} \right) \right)^2$$

Under the condition that $R_q = 21.73K$ ohm, $\omega_{Nr} = 1.1$.

The output power supplied to the load P_{out} is as presented in Eq. (15). It needs to do compound multiplication in accumulation to significant the phase variance α_i among the voltage & current.

The system's efficiency is, $\eta = \left(\frac{P_{out}}{P_{in}} \right) 100$

$$= \left(\frac{2\sqrt{2} \cdot V_{dc}^2 \cos^2 \left(\frac{\Phi}{2} \right) \cdot R_q \cdot P_{in}}{(\pi^2) \cdot \left(1 + P^2 \right) \cdot \left(\omega_{Nr} - \left(\frac{1}{\omega_{Nr}} \right) \right)^2} \right) 100$$

The inverter efficiency is, $\eta_{inv} = \left(\frac{P_{inv} - ac}{P_{rec} - dc} \right) 100$

$$= \left(\frac{4700}{5260} \right) 100 = 89.35\%$$

To determine the output power delivered to the load P_{out} is as shown in Eq. (15). It needs to do complex multiplication in accumulation to significant the phase variance α_i between the voltage and current.

The system's efficiency is,

$$\eta = \left(\frac{P_{out}}{P_{in}} \right) 100 = \left(\frac{5000}{5260} \right) 100 = 95\% \dots (16)$$

To straight determine the output power P_{out} as presented in Eq. (17), it wants to complex multiplication in addition to expressive the phase difference α_i among voltage and current. In the present article, one used the fitting curve to analyze the output power with simple adding and deduction procedures only. With this technique, it can get the estimated power by simple scheming steps & can speed up the calculation procedure.

$$P_{out} = K_v \cdot V_{o,rms} + K_i \cdot i_{o,rms} - K\Phi_i \cdot \Phi_i \dots (17)$$

The electrical phase is calculated in degrees with 360 degrees consistent to the whole cycle. A sinusoidal voltage is related to the cosine or sine of the phase.

$$\text{The phase angle } A_T = A_{max} * \sin (\omega_t \pm \Phi)$$

where, A_{max} = amplitude of the signal; ω_t = angular frequency of the signal; Φ = phase angle in degree or radians that the signal has moved either left or right from the location point.

The ozone production with different input voltage and applied voltage flow rates are presented in Fig. 6. The result presents that the ozone production starts at a voltage of 7.0 kV_{p-p} (210 V input voltage); from 7.0 kV_{p-p} (210 V input voltage) the ozone production kept increasing in each voltage rate. The high ozone production is noted at 10 kV_{p-p} (240 V input voltage) for different flow rates.

Performance of the Developed Model with Respect to Simulation Model

Finally, we consider the results between the developed model with respect to the simulation model by measuring the AC output power of DBD AC power supply. We measured the developed model results by using regulation and without regulation circuit. We designed a line regulation circuit with an optoisolated feedback circuit. The DC power generated by the bridge rectifier while varying AC input voltage and V_{feed} also varied in the regulation

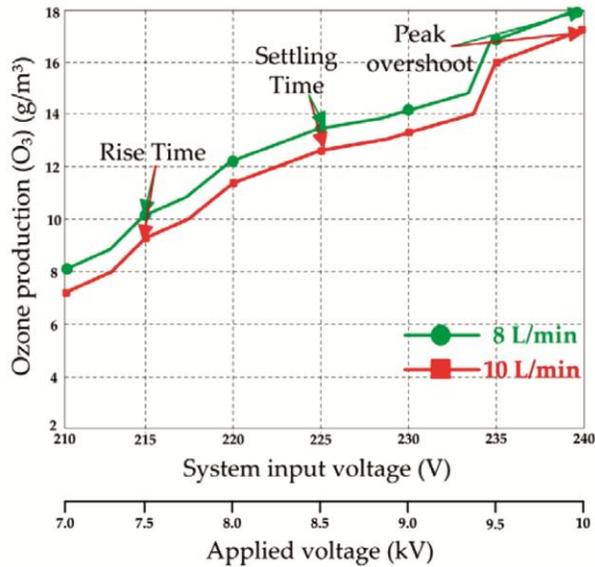


Fig. 6 — Ozone production vs different input voltage and applied voltage flow rates

Table 2 — Variation at the DC output and V_{feed} in with and without regulation circuit

Input V_{AC}	Without line regulation		With line regulation	
	Output V_{DC}	V_{feed}	Output V_{DC}	V_{feed}
240	310	2.64	299	2.78
230	299.5	2.64	291.3	2.74
220	290	2.64	290	2.64
210	273	2.64	285	1.99
200	258	2.64	273	0.89

circuit. The differentiation of the DC output power and V_{feed} are given in Table 2.

The developed model electrical waveforms recorded by Teledyne Lecroy (350 MHz, 2 GS/s). The voltage pulse was measured by Teledyne Lecroy high voltage probe. The current was measured by a current monitor. The simulation model was simulated by P_{SIM} (Power SIM). The results of the developed model and theoretical values are almost matched. The developed model and simulation model of AC power generations during experimental and simulations were compared in Table 3.

Performance Analysis

The prototype of the digital controlled DBD AC power supply was constructed as described in previous sections. A complete picture of the power supply can be seen in Figs 1 and 2. Among the research of DBD, simulation is one of the common and useful ways. The simulation results of the DBD power supply are shown in Fig. 4. Further information

Table 3 — Comparison of developed model and simulation model AC power generations

Input AC Voltage	Developed Model (AC Power Generation)	Simulation Model (AC Power Generation)
240	4.6 kW	4.57 kW
230	4.57 kW	4.52 kW
220	4.54 kW	4.49 kW
210	4.5 kW	4.45 kW
200	4.48 kW	4.4 kW

Table 4 — Quality indices

Controller	Peak overshoot	Settling time	Rise time
STM microcontroller	52.51	7	9

Table 5 — Error indices

Controller	Error indices (Designed System)			
	ISE	IAE	ITSE	ITAE
STM microcontroller	64.50	12.15	120.25	5465

Here, ISE = Integral Square Error; IAE = Integral Absolute Error; ITSE = Integral Time Square Error; ITAE = Integral Time Absolute Error

about simulation results and parameters are also given. But, simulation technique is more complicated and time-consuming when used for long period and for different designs and models.

The line regulation circuit used to regulate the output power due to the zero voltage detector circuit. The output power decreases with an increased operating frequency and a zero voltage detector circuit is achieved for the whole power range. The peak applied voltage plays an important role in ozone generation.

When the resonant tank works at resonance, it is resistive and later the resonant frequency can be derivative. The operating frequency and tank gain at resonance with variant C20. The resonance frequency increases with decreased C20, which results in high power density. However, the tank gain of the resonant converter diminishes in the meantime.

The quality indices are presented in Table 4 for overshoot, settling time and peak overshoot and rise time for the proposed STM controller. The STM controller has quite better in case of performance. The settling time of the STM controller is 7 seconds and the rise time is 9 seconds when compared with the other controllers.⁵¹

Likewise, error indices are presented in Table 5 for the STM controller in terms of ISE, IAE, ITSE, ITAE. These indices are analyzing the good performance of the controller.

Conclusions

This study evaluated a dielectric barrier discharge AC power supply with a digital control system for ozone generation. The presented DBD system was controlled with digital control and pulse gate driver circuit. The working frequency of the PWM inverter was 25 kHz. Zero voltage detection could be reached and the efficiency was improved. Lastly, a design with 4.6 kW and 10 kVp-p was produced in the laboratory. A step-up transformer was also added to restrict the determined voltage, and under-voltage, condition operation could be avoided. The DBD AC power supply verified experimentally and achieved maximum efficiency of 87.4% at 4.6 kW/10 kVp-p.

The novelty of this system is designed with a low cost of microcontroller which is an STM microcontroller. The efficiency of the controlled system is quite high compare with the other technologies. We address the driver circuits and control circuits in this article, these both circuits are used to synchronize the microcontroller with the AC input signals and the ability of the power supply to preserve its listed output voltage over the deviations in the input line voltage. This is stated as the percentage variation in the output voltage compared to the variation in the input line voltage.

Future Scope

The present research work successfully verified and in the future, we are going to design a 60 kV high voltage power supply for commercial applications in the private and public sectors. We are trying to achieve more efficiency and good stability than previously designed power supplies.

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